Complementary Plastic Silicon Power Transistors

The MJE170/180 series is designed for low power audio amplifier and low current, high speed switching applications.

Features

• Collector-Emitter Sustaining Voltage -

V_{CEO(sus)} = 40 Vdc - MJE170, MJE180 = 60 Vdc - MJE171, MJE181 = 80 Vdc - MJE172, MJE182

• DC Current Gain -

 $h_{FE} = 30 \text{ (Min)} @ I_C = 0.5 \text{ Adc}$ = 12 (Min) @ I_C = 1.5 Adc

• Current-Gain - Bandwidth Product -

 $f_T = 50 \text{ MHz (Min)} @ I_C = 100 \text{ mAdc}$

• Annular Construction for Low Leakages -

 $I_{CBO} = 100 \text{ nA (Max)}$ @ Rated V_{CB}

• Epoxy Meets UL 94 V-0 @ 0.125 in

• ESD Ratings: Machine Model, C Human Body Model, 3B

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Base Voltage MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	V _{CB}	60 80 100	Vdc
Collector-Emitter Voltage MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	V _{CEO}	40 60 80	Vdc
Emitter-Base Voltage	V _{EB}	7.0	Vdc
Collector Current – Continuous – Peak	I _C	3.0 6.0	Adc
Base Current	I _B	1.0	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	12.5 0.012	W W/°C
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.5 0.1	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1



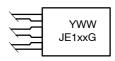
ON Semiconductor®

http://onsemi.com

3 AMPERES POWER TRANSISTORS COMPLEMENTARY SILICON 40 - 60 - 80 VOLTS 12.5 WATTS



MARKING DIAGRAM



Y = Year

WW = Work Week

1xx = Specific Device Code

x = 70, 71, 72, 80, 81, or 82

G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	θЈС	10	°C/W
Thermal Resistance, Junction-to-Ambient	θЈА	83.4	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25$ °C unless otherwise noted)

	,				
Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Sustaining Voltage (I _C = 10 mAdc, I _B = 0)	MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	V _{CEO(sus)}	40 60 80	_ _	Vdc
	MJE170, MJE180 MJE171, MJE181 MJE172, MJE182 MJE170, MJE180 MJE171, MJE181 MJE172, MJE182	Ісво	- - - -	0.1 0.1 0.1 0.1 0.1	μAdc mAdc
Emitter Cutoff Current (V _{BE} = 7.0 Vdc, I _C = 0)		I _{EBO}	-	0.1	μAdc
ON CHARACTERISTICS					
DC Current Gain $(I_C = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_C = 500 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_C = 1.5 \text{ Adc}, V_{CE} = 1.0 \text{ Vdc})$		h _{FE}	50 30 12	250 - -	-
Collector–Emitter Saturation Voltage ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$) ($I_C = 1.5 \text{ Adc}$, $I_B = 150 \text{ mAdc}$) ($I_C = 3.0 \text{ Adc}$, $I_B = 600 \text{ mAdc}$)		V _{CE(sat)}	- - -	0.3 0.9 1.7	Vdc
Base-Emitter Saturation Voltage ($I_C = 1.5$ Adc, $I_B = 150$ mAdc) ($I_C = 3.0$ Adc, $I_B = 600$ mAdc)		V _{BE(sat)}	_ _	1.5 2.0	Vdc
Base-Emitter On Voltage (I _C = 500 mAdc, V _{CE} = 1.0 Vdc)		V _{BE(on)}	_	1.2	Vdc
DYNAMIC CHARACTERISTICS		•	•	•	
Current-Gain - Bandwidth Product (Note 1) (I _C = 100 mAdc, V _{CE} = 10 Vdc, f _{test} = 10 MHz)		f _T	50	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 MHz)	MJE171/MJE172 MJE181/MJE182	C _{ob}	_ _	60 40	pF

^{1.} f_T = |h_{fe}| • f_{test}.

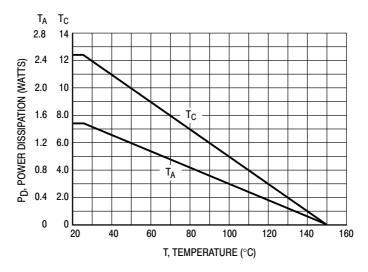
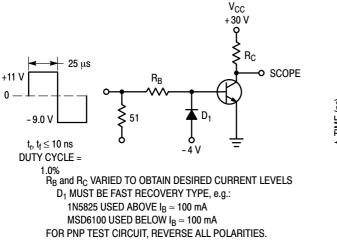


Figure 1. Power Derating

500



 $I_C/I_B = 10$ 300 200 $V_{BE(off)} = 4.0 V$ $T_{.l} = 25^{\circ}C$ 100 t, TIME (ns) 50 30 20 t_d 10 NPN MJE181/182 PNP MJE171/172 0.02 0.03 0.05 0.1 0.2 0.3 0.5 3 IC, COLLECTOR CURRENT (AMPS)

Figure 2. Switching Time Test Circuit

Figure 3. Turn-On Time

V_{CE} = 30 V

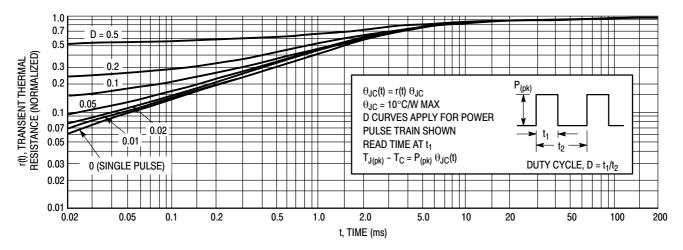


Figure 4. Thermal Response

ACTIVE-REGION SAFE OPERATING AREA

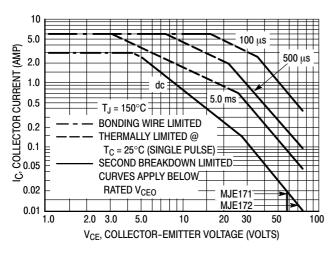


Figure 5. MJE171, MJE172

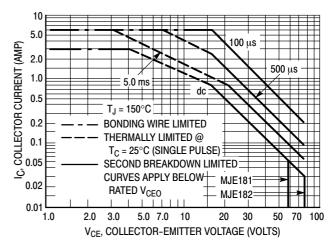


Figure 6. MJE181, MJE182

There are two limitations on the power handling ability of a transistor – average junction temperature and second breakdown. Safe operating area curves indicate I_C – V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 5 and 6 is based on $T_{J(pk)} = 150^{\circ} C$; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 150^{\circ} C$. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperature, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

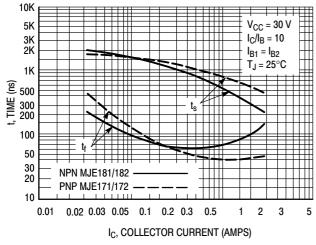


Figure 7. Turn-Off Time

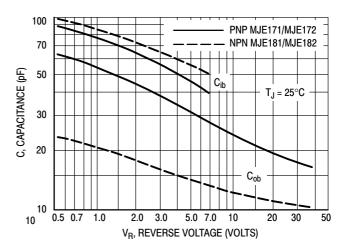


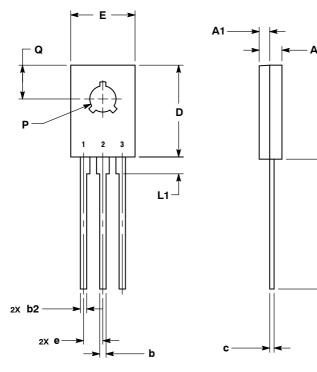
Figure 8. Capacitance

ORDERING INFORMATION

Device	Package	Shipping
MJE170	TO-225	
MJE170G	TO-225 (Pb-Free)	
MJE171	TO-225	
MJE171G	TO-225 (Pb-Free)	
MJE172	TO-225	
MJE172G	TO-225 (Pb-Free)	
MJE180	TO-225	500 Units / Box
MJE180G	TO-225 (Pb-Free)	
MJE181	TO-225	
MJE181G	TO-225 (Pb-Free)	
MJE182	TO-225	
MJE182G	TO-225 (Pb-Free)	

PACKAGE DIMENSIONS

TO-225 CASE 77-09 ISSUE AA



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. NUMBER AND SHAPE OF LUGS OPTIONAL.

$\overline{}$			
	MILLIMETERS		
DIM	MIN	MAX	
Α	2.40	3.00	
A1	1.00	1.50	
b	0.60	0.90	
b2	0.51	0.88	
С	0.39	0.63	
D	10.60	11.10	
E	7.40	7.80	
е	2.04	2.54	
L	14.50	16.63	
L1	1.27	2.54	
P	2.90	3.30	
Q	3.80	4.20	

STYLE 1:

PIN 1. EMITTER

COLLECTOR 2. 3.

BASE

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